L1CFT Trigger Terms to L1Muo

The format of each sixteen-bit word is as follows:

Bits	Content
[05]	Hcent
[610]	Alayer Offset / PT
11	Sign
[1214]	Spare
15 (most signifigant)	Track Found

Look-up table for P_T Information:

P _T Threshold	Bits109	Bits 86	Offset / P _T Ave
D 1 (Lowest)			
P _T 1 (Lowest)	11	111	14
	11	110	13
		101	12
		100	11
		011	10
		010	9
		001	8
		000	7
$P_T 2$			
	10	011	6
		010	5
		001	4 3
		000	3
P _T 3			
r _T 3	01	011	5.33
	O1	010	6.15
		001	7.27
		000	8.89
		000	0.07
P _T 4 (Highest)			
• (8•)	00	011	11.43
		010	16.0
		001	26.67
		000	80.0

Questions:

- 1. Is the PT information actually on five bits, and not six?
- 2. Will the upper two bits always be used to designate four bins, as above?
- 3. Is it possible to get PT values for the lower two bins?